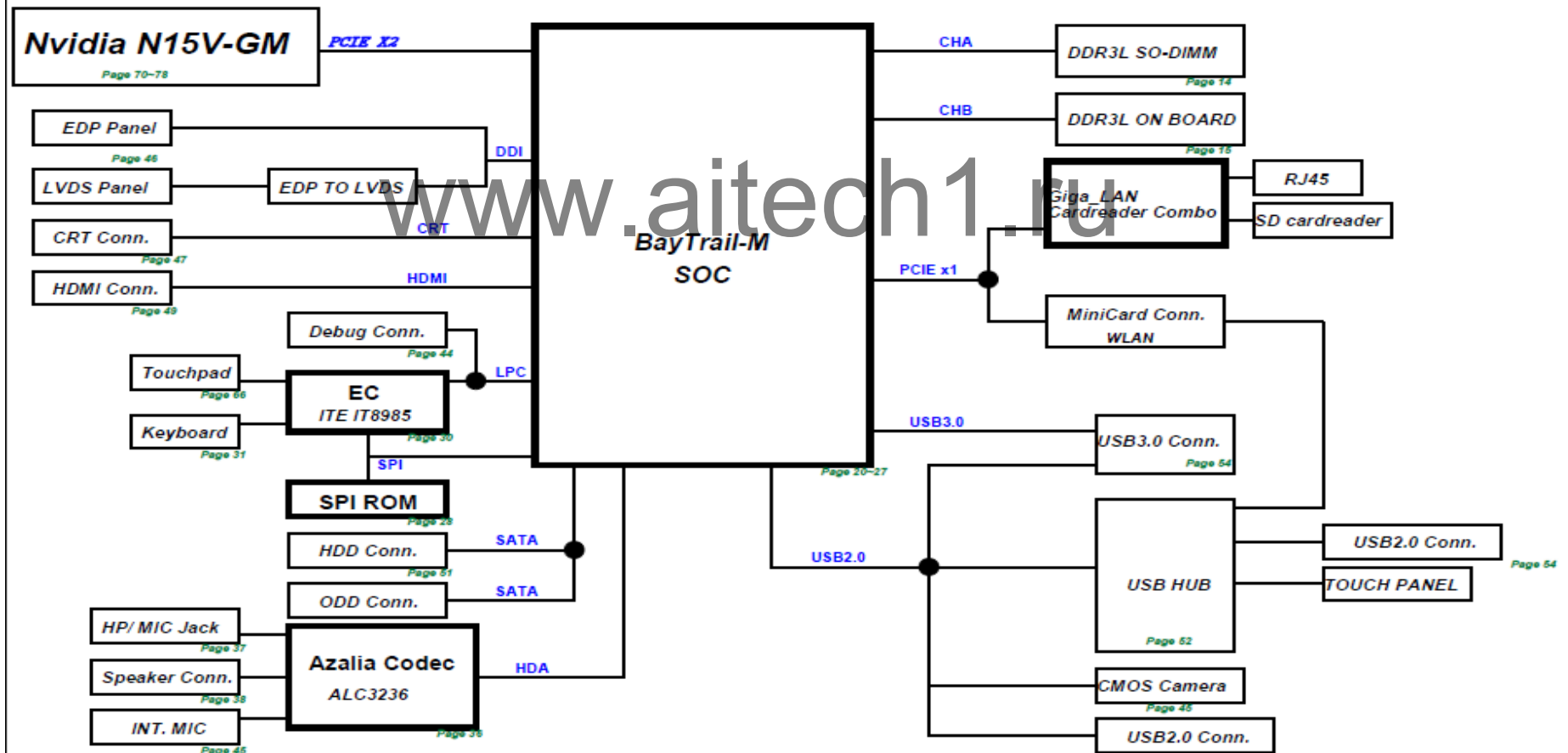
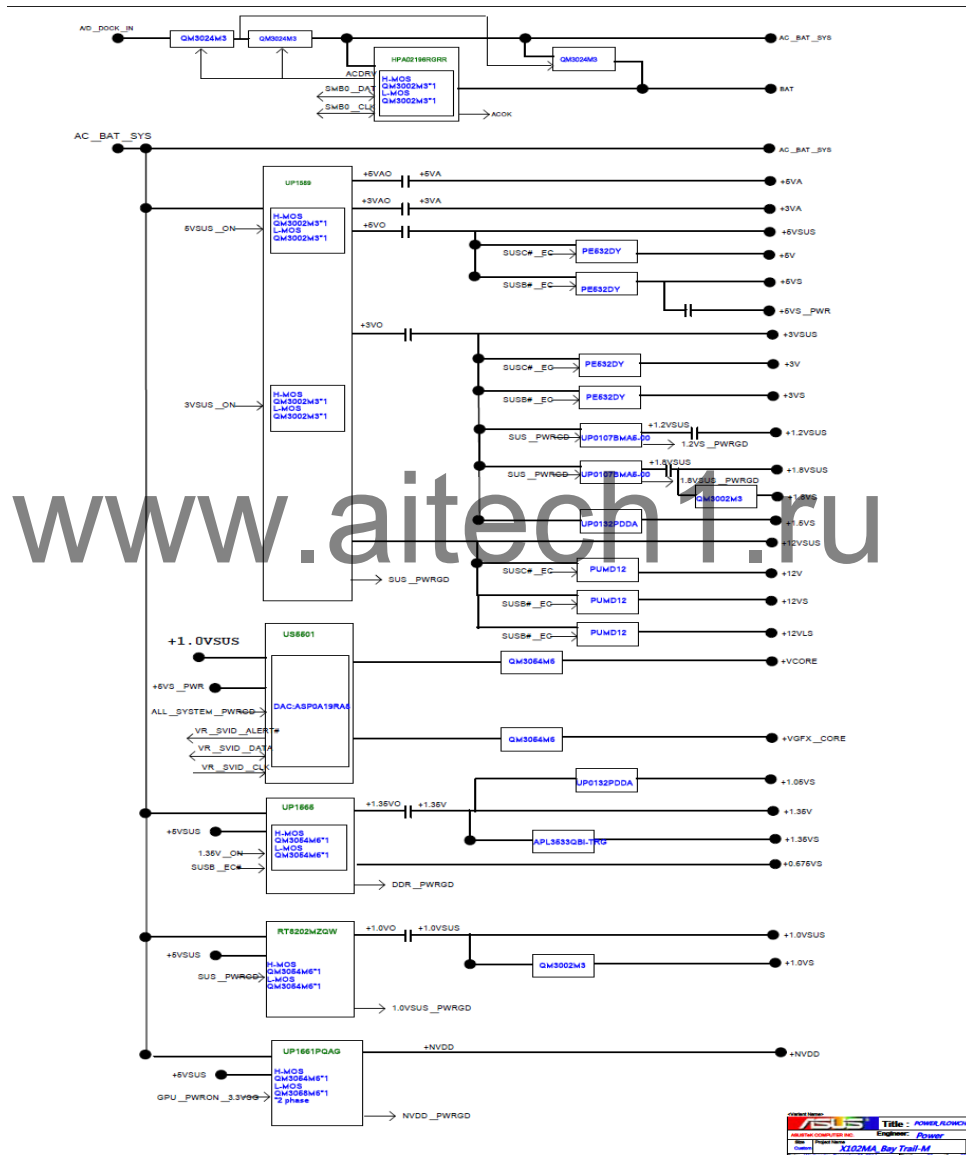


BLOCK DIAGRAM

X751MA/MD BLOCK DIAGRAM



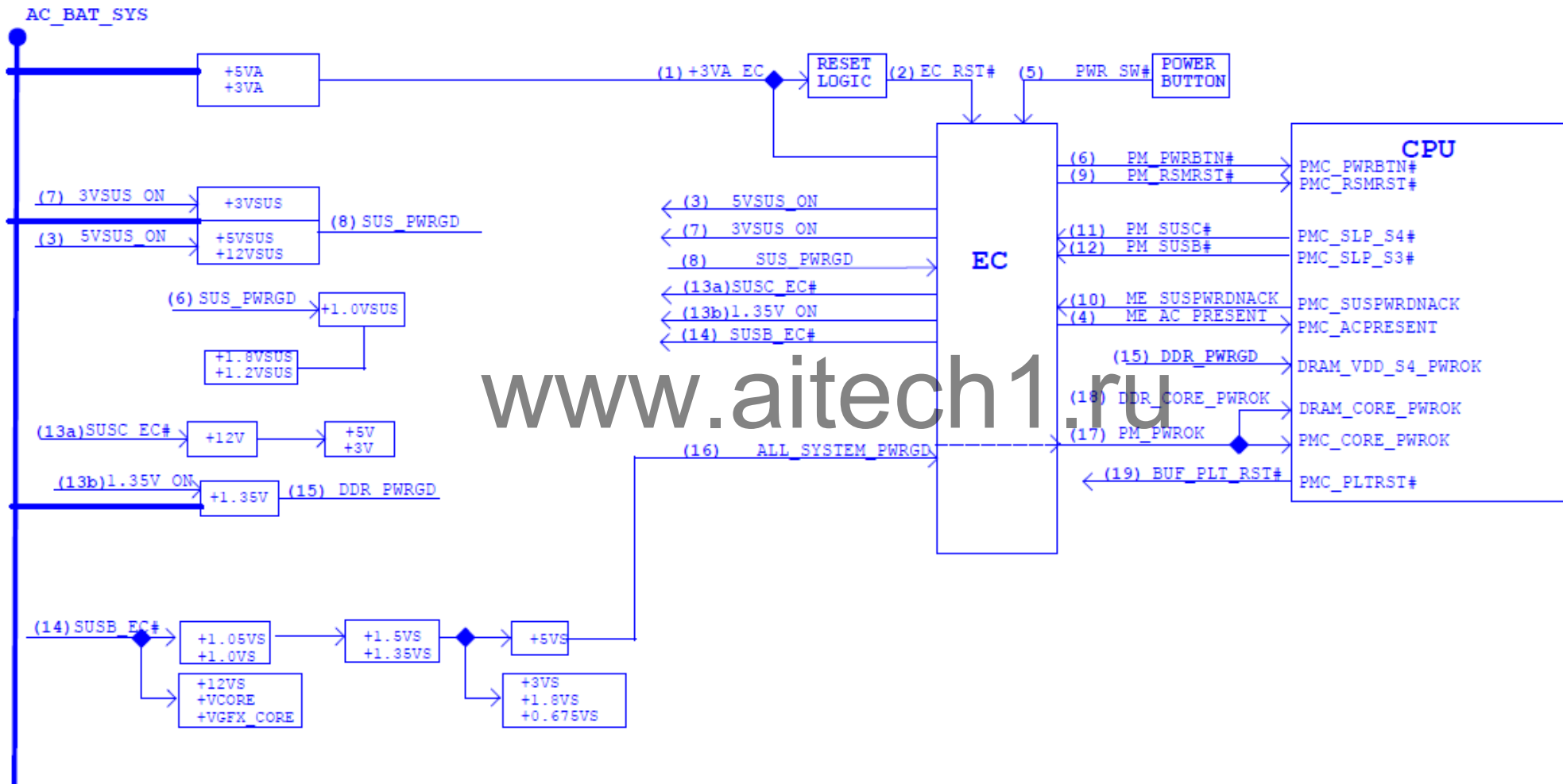
POWER FLOW



POWER ON SEQUENCE

AC-IN Mode

X751MJ Power On Sequence Diagram Rev.0.1

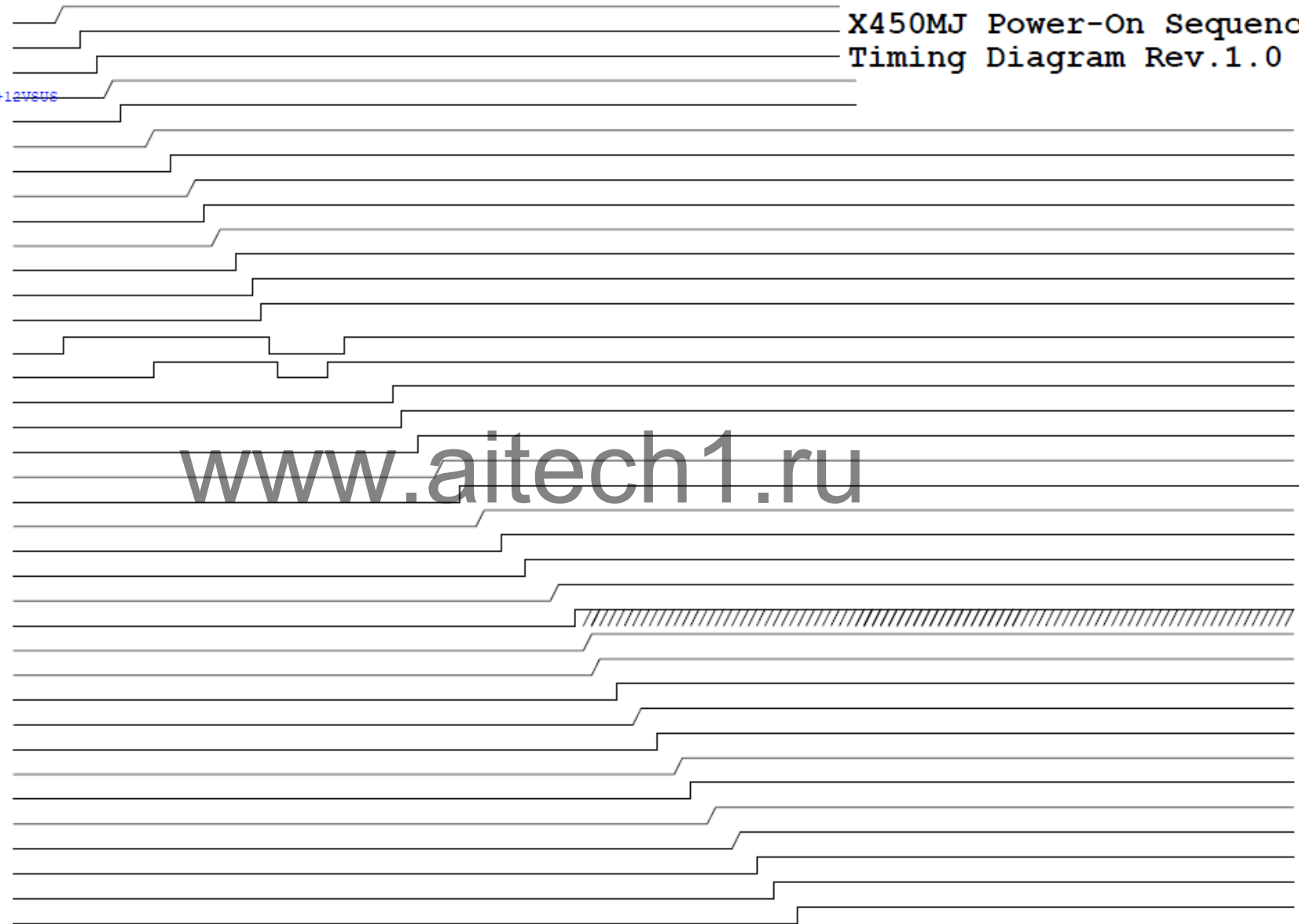


AC_IN POWER ON SEQUENCE

AC-IN
Mode

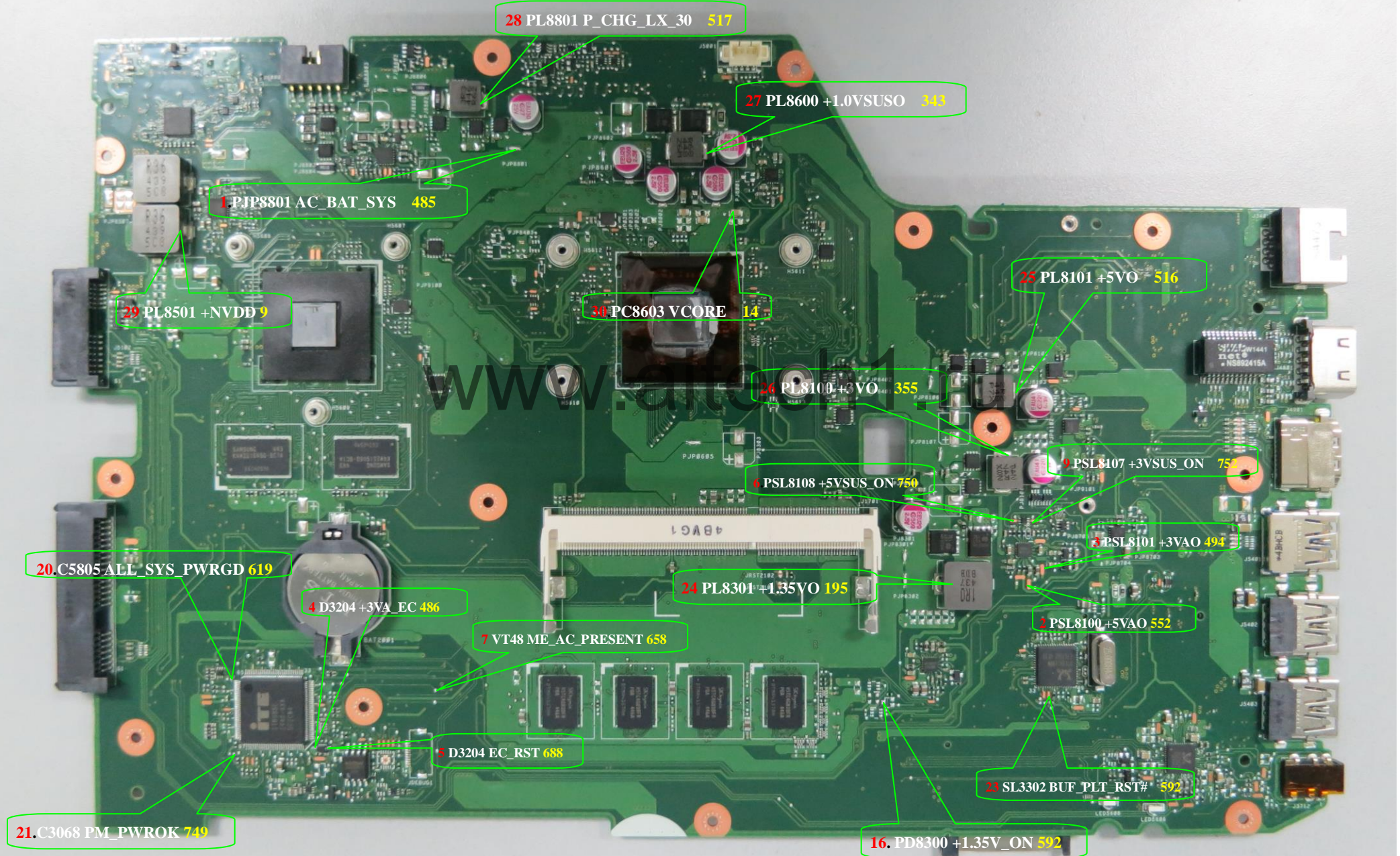
X450MJ Power-On Sequence
Timing Diagram Rev.1.0

1 +3VA/+5VA/+3VA_EC
2 EC_RST#
3 +5VSUS_ON
+5VSUS/+12VSUS
4 +3VSUS_ON
+3VSUS
5 SUS_PWRGD
+1.8VSUS/+1.0VSUS
6 1.0SUS_PWRGD
+1.2VSUS
7 PM_RSMRST#
8 ME_SusPwrDnAck
9 ME_AC_PRESENT
10 PWR_SW#
11 PM_PWRBTN#
12 PM_SUSC#
13 PM_SUSB#
14 SUSC_EC#
+12V/+5V/+3V
15 +1.35V_ON
+1.35V
16 DDR_PWRGD
17 SUSB_EC#
+12VS
18 SVID
VFGX/VCORE
+5VS_C
19 VRM_PWRGD
+1.05VS
20 +1.05VS_PWRGD
+1.35VS/+1.5VS
21 +1.5VS_PWRGD
+0.675VS/+12VLS
+5VS/+3VS/+1.8VS/+1.2VS/+1.0VS
22 PM_PWROK
23 ALL_SYSTEM_PWRGD
24 BUF_PLT_RST#



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Signal Measure Point-Bottom



Signal Measure Point-Top

